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 Johan Pouwelse , Koen Langendoen , Henk Sips
Proceedings of the seventh annual international conference on Mobile computing and networking July 2001

Power consumption is the limiting factor for the functionality of future wearable devices. Since interactive applications like wireless information access generate bursts of activities, it is important to match the performance of the wearable device accordingly. This paper describes a system with a microprocessor whose speed can be varied (frequency scaling) as well as its supply voltage. Voltage scaling is important for reducing power consumption to very low values when operating at low spee ...


- 6** Oracle media server: providing consumer based interactive access 83%
 to multimedia data
Andrew Laursen , Jeffrey Olkin , Mark Porter
ACM SIGMOD Record , Proceedings of the 1994 ACM SIGMOD international conference on Management of data May 1994
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Currently, most data accessed on large servers is structured data stored in traditional databases. Networks are LAN based and clients range from simple terminals to powerful workstations. The user is corporate and the application developer is an MIS professional. With the introduction of broadband communications to the home and better than 100-to-1 compression techniques, a new form of network-based computing is emerging. Structured data is still important, but the bulk of data b ...


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 Control-theoretic dynamic frequency and voltage scaling for multimedia workloads
Zhijian Lu , Jason Hein , Marty Humphrey , Mircea Stan , John Lach , Kevin Skadron
Proceedings of the international conference on Compilers, architecture, and synthesis for embedded systems October 2002
This paper describes a formal feedback-control algorithm for dynamic voltage/frequency scaling (DVS) in a portable multimedia system to save power while maintaining a desired playback rate. Our algorithm is similar in complexity to the previously-proposed change-point detection algorithm [19] but does a better job of maintaining stable throughput and is not dependent on the assumption of an exponential distribution of the frame decoding rate. For approximately the same energy savings as reported ...
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Paul H. Dietz , William S. Yerazunis
Proceedings of the 14th annual ACM symposium on User interface software and technology November 2001
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 Anna Haj Ha? , Cindy X. Xue
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Synchronization of multiple medium streams in real time has been recognized as one of the most important requirements for multimedia applications based on broadband high-speed networks. This article presents a complete synchronization scheme for distributed multimedia information systems. © 1997 John Wiley & Sons, Ltd.
- 10** A reliable multicast framework for light-weight sessions and 82%
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 Tzi-cker Chiueh , Randy H. Katz
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September 1993


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 Simon Gibbs
ACM SIGPLAN Notices , Conference proceedings on Object-oriented
programming systems, languages, and applications November 1991
Volume 26 Issue 11

13 Management of a remote backup copy for disaster recovery 82%

 Richard P. King , Nagui Halim , Hector Garcia-Molina , Christos A. Polyzois
ACM Transactions on Database Systems (TODS) May 1991
Volume 16 Issue 2
A remote backup database system tracks the state of a primary system, taking over transaction processing when disaster hits the primary site. The primary and backup sites are physically isolated so that failures at one site are unlikely to propagate to the other. For correctness, the execution schedule at the backup must be equivalent to that at the primary. When the primary and backup sites contain a single processor, it is easy to achieve this property. However, this is harder to do when ...

14 Performance verification and system parameter identification of 82%

 spacecraft tape recorder control servo
Asok K. Mukhopadhyay
Proceedings of the twelfth annual simulation symposium March 1979
NASA's 1976 Viking Mars Orbiters and the Voyager 1977
Jupiter-Saturn flyby spacecrafts carried digital tape recorders (DTR) on board. The recorders for both the missions were very similar except for the use of a brushless d.c. motor in the Voyager DTR. The mechanical part of the DTR consisted of fourteen rotating masses (tape reels, idlers and drive capstans) distributed in different planes and connected by three different elastic couplings (magnetic tape, peripheral belt and drive belts). ...

15 Papers: Physically embodied video snippets supporting 82%

 collaborative exploration of video material during design sessions
Tomas Sokoler , Håkan Edeholt

Proceedings of the second Nordic conference on Human-computer interaction October 2002

In this paper we explore the idea of using physically embodied video snippets as an alternative to today's means for control of video playback during collaborative design oriented meetings. We aim to make video snippets a more integral part of the shared resources and opportunities for action already present at brainstorm like meetings. We present our VideoTable and VideoCards. The VideoTable is an augmented meeting table. The VideoCards are paper card representations of video snippets embedding ...

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Hemang Lavana , Amit Khetawat , Franc Brglez

Proceedings of the international ACM SIGGROUP conference on Supporting group work : the integration challenge: the integration challenge November 1997

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Brian W. Unger , Greg A. Lomow

Proceedings of the 25th conference on Winter simulation December 1993

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 graphics hardware

Jed Lengyel , Mark Reichert , Bruce R. Donald , Donald P. Greenberg

ACM SIGGRAPH Computer Graphics , Proceedings of the 17th annual conference on Computer graphics and interactive techniques September 1990

Volume 24 Issue 4

19 Real time animation playback on a frame store display system 80%

 Bryan Ackland , Neil Weste

Proceedings of the 7th annual conference on Computer graphics and interactive techniques July 1980

A frame store display station capable of generating real time animation effects using filled polygonal shapes is described. The system consists of a host microcomputer, a high performance frame store display controller and a microprogrammed graphic command interpreter. Real time performance is attained using a combination of software, hardware and a systems approach. The display station can be used to interactively test and view simple animation sequences. Some proposals for further increas ...

20 Network Issues for Video and Games: Adaptive rate control for 80%
streaming stored fine-grained scalable video

Philippe de Cuetos , Keith W. Ross

Proceeding of the 12th International Workshop on Network and
Operating Systems Support for Digital Audio and Video May 2002

In this paper we investigate adaptive streaming of stored fine-grained scalable video over a TCP-friendly connection. The goal is to develop low-complexity yet high-performing schemes that adequately adapt to the short- and long-term variations in available bandwidth. We first present a novel framework for low-complexity streaming of fine-grained scalable video over a TCP-friendly connection. In the context of this scheme, and under the assumption of complete knowledge of bandwidth evolution, we ...

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 Katherine M. Shelfer , J. Drew Procaccino
Communications of the ACM July 2002
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A self-assessment procedure dealing with computer organization and
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- 6 InfoPoint: A Device that Provides a Uniform User Interface to Allow 80%
4 Appliances to Work Together over a Network
Naohiko Kohtake , Jun Rekimoto , Yuichiro Anzai
Personal and Ubiquitous Computing January 2001
Volume 5 Issue 4
This paper proposes a new hand-held device called
“InfoPoint” that allows appliances to work together
over a network. We have applied the idea of
“drag-and-drop” operation as provided in the GUIs of
PC and workstation desktop environment. InfoPoint provides a unified
interface that gives different types of appliances
“drag-and-drop”-like behaviour for the transfer of data.
Moreover, it can transfer data from/to non-appliances such as pieces
of paper. As a resu ...
- 7 Pen computing: a technology overview and a vision 80%
4 André Meyer
ACM SIGCHI Bulletin July 1995
Volume 27 Issue 3
This work gives an overview of a new technology that is attracting
growing interest in public as well as in the computer industry itself.
The visible difference from other technologies is in the use of a pen
or pencil as the primary means of interaction between a user and a
machine, picking up the familiar pen and paper interface metaphor.
From this follows a set of consequences that will be analyzed and put
into context with other emerging technologies and visions.Starting
with a short historic ...
- 8 Dynabook revisited—portable computers past, present and 80%
4 future

Larry Press
Communications of the ACM March 1992
Volume 35 Issue 3

9 The architecture of a fault-tolerant cached RAID controller 80%


 Jai Menon , Jim Cortney

ACM SIGARCH Computer Architecture News , Proceedings of the 20th annual international symposium on Computer architecture May 1993
Volume 21 Issue 2

RAID-5 arrays need 4 disk accesses to update a data block—2 to read old data and parity, and 2 to write new data and parity.

Schemes previously proposed to improve the update performance of such arrays are the Log-Structured File System [10] and the Floating Parity Approach [6]. Here, we consider a third approach, called Fast Write, which eliminates disk time from the host response time to a write, by using a Non-Volatile Cache in the disk array controller. We examine three alternativ ...

10 An instruction-level performance analysis of the Multiflow TRACE 80%

 14/300

Michael A. Schuette , John P. Shen

Proceedings of the 24th annual international symposium on Microarchitecture September 1991

11 Criteria for computer performance analysis 77%

 Jozo J. Dujomovi?

Proceedings of the 1979 ACM SIGMETRICS conference on Simulation, measurement and modeling of computer systems August 1979

Computer evaluation, comparison, and selection is essentially a decision process. The decision making is based on a number of worth indicators, including various computer performance indicators. The performance indicators are obtained through the computer performance measurement procedure. Consequently, this procedure should be completely conditioned by the decision process. This paper investigates various aspects of computer performance measurement and evaluation procedure withi ...

12 An automatic system approach to the problem of memory circuit 77%

 testing & fault diagnosis

Peter S. Bottorff , Mitchel E. Schwass , Francis J. Villante

Proceedings of the June 1970 design automation workshop on Design automation June 1970

High product volume, diverse packaging configurations, and high-speed functional operation make memory circuit testing a difficult manufacturing problem. One solution to this problem is

provided by the computer-controlled test system described here. Modular design of hardware, microprogram, and programming subsystems has met production requirements for speed, quality, and repair of assembly defects.

13 A work-training program for developing consultants at the 77%


 University of Missouri - St. Louis

William P. Heinbecker

Proceedings of the 5th annual ACM SIGUCCS conference on User services November 1977

Several years ago the University of Missouri - St. Louis (UMSL) Campus Computer Center initiated a four year work-training program for incoming freshmen. The purpose of the program was to develop student assistants who could relieve the full-time staff of some of the consulting load. It was also intended that the consulting services could be expanded to serve students, who at the time were almost entirely dependent on instructors for assistance. The initial group began in the fall of 1975 a ...


14 Microcomputer access to cross software systems 77%

 Bernard J. Carey , Anne-Marie G. Discepolo

Proceedings of the 3rd ACM SIGSMALL symposium and the first SIGPC symposium on Small systems September 1980

Downline loading involves the transferral of information from the internal memory of one computer to the internal memory of another. For development microcomputer systems utilizing cross assemblers (i.e. non-resident assemblers), downline loading provides an extremely fast method of transferring cross-assembled files into the microprocessor's memory. The design for one low-cost method of linking a microcomputer system to a larger computer for downline loading is described. In this design, t ...

15 The big three - today's 16-bit microprocessor 77%

 R. K. Bell , W. D. Bell , T. C. Cooper , T. K. McFarland

Proceedings of the 13th micro-programming workshop on Microprogramming November 1980

This paper reports on the functional evaluation of the three 16-bit microprocessors, namely the Intel 8086, the Zilog Z8000, and the Motorola MC68000. These microprocessors were employed in several CRT applications, both monochrome and color. Execution time benchmark tests were made, mechanization problems compared and instruction/architectural characteristics highlighted. Conclusions and recommendations are made applicable to terminals and similar Sperry Univac products.

16 Two implementations of the 'FLEX' machine 77% John Kershaw

Proceedings of the 14th annual microprogramming workshop on
Microprogramming December 1981


The FLEX high-level language architecture is introduced. Two microprogrammed implementations of FLEX are described, one based on a special purpose, horizontally coded machine and the other on a general purpose emulator called GEMINI. Examples and statistics of both microprograms are given and the two implementations compared.

17 Must-have Zaurus hardware and software 77% Gylhem Aznar

Linux Journal January 2002

Volume 2003 Issue 105

You've already impressed everyone with your PDA's sliding keyboard--now impress them with movies, chat and more.

18 The Roma personal metadata service 77% Edward Swierk , Emre Kiciman , Nathan C. Williams , Takashi Fukushima , Hideki Yoshida , Vince Laviano , Mary Baker

Mobile Networks and Applications October 2002

Volume 7 Issue 5

People now have available to them a diversity of digital storage facilities, including laptops, cell phone address books, handheld devices, desktop computers and web-based storage services. Unfortunately, as the number of personal data repositories increases, so does the management problem of ensuring that the most up-to-date version of any document in a user's personal file space is available to him on the storage facility he is currently using. We introduce the Roma personal metadata service t ...

19 Architecture and implementation of a VLIW supercomputer 77% Robert P. Colwell , W. Eric Hall , Chandra S. Joshi , David B. Papworth , Paul K. Rodman , James E. Tornes

Proceedings of the 1990 ACM/IEEE conference on Supercomputing
November 1990

Very-Long-Instruction-Word (VLIW) computers achieve high performance by exploiting the fine-grain parallelism present in sequential or vectorizable code. Multiflow's /200 and /300 VLIW systems yielded near-supercomputer performance by this means despite the relatively slow (65 nS) clocks. With its much faster clock period (15 nS) and architectural improvements, the new /500 system attains approximately 4-9X the performance of its predecessors. This paper describes the /500 architecture and implem ...

20 Cache coherence in systems with parallel communication channels 77%
& many processors



John C. Willis , Arthur C. Sanderson , Charles R. Hill

Proceedings of the 1990 ACM/IEEE conference on Supercomputing

November 1990

This paper describes and analyzes two algorithms for maintaining cache coherence in multiprocessor systems with parallel communication channels and many processors. A distributed link-list relates all cache frames representing the same main memory block. Messages traverse the list to maintain list integrity, exclusive ownership, and consistent values. Memory access semantics are equivalent to a shared memory system without caches. Reference latency, efficiency of memory use, and hardware complex ...

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Jinfeng Liu , Pai H. Chou , Nader Bagherzadeh , Fadi Kurdahi
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Michael Beigl , Tobias Zimmer , Christian Decker
Personal and Ubiquitous Computing January 2002
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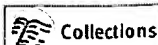
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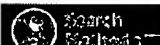
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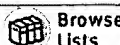
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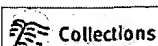
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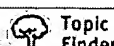
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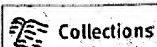
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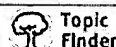
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